Registration Form

Name of Applicant:	
Designation:	
Date of Birth: (DD/MM/YY):	
Gender M F	
Category (Gen/OBC/SC/ST)	
Name and Address of the Institute/ Organization:	
Email:	•
Mobile No	



Celebrating 75 Years of Excellence: Platinum Jubilee of BIT Sindri

In the heart of Jharkhand, India, stands an institution that has been a beacon of technical education for the past 75 years. The proud alma mater of countless engineers and industry leaders, BIT Sindri has reached a significant milestone – its Platinum Jubilee. To mark the 75th year, BIT Sindri is hosting a series of events that go beyond academic achievements. Workshop, Lecture Series, International Conferences, Cultural festivals, sports competitions, and alumni meets are just a few of the festivities planned.

ABOUT THE INSTITUTE

BIT, Sindri, was started as the College of Mechanical and Electrical Engineering in 1949. The institute is located at a distance of 28 km from Dhanbad railway station, which is linked by rail as well as road. It has a sprawling campus of about 450 acres of land near the eastern bank of the Damodar River. The institute is administratively controlled by the Department of Higher Technical Education, Government of Jharkhand, and academically affiliated with Jharkhand University of Technology, Ranchi. All courses are approved by AICTE, and most of the undergraduate programmes are accredited by the NBA.

The college offers B.Tech courses in ten disciplines of engineering, ten M.Tech specializations, and Doctorate. The college possesses modern amenities, which include multimedia auditoriums, seminar rooms, class rooms, a state-of-the-art, well-stocked, rich E-library, well-equipped modern laboratories, a campus-wide network, and a state-of-the-art Siemens lab, which is regarded as a Centre of









ONE DAY

WORKSHOP

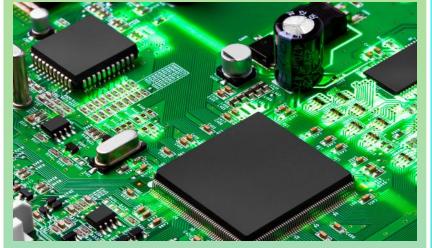
ON

SOFTWARE DEFINED RADIO Under the Aegis of Platinum Jubilee Celebration Year

15th June 2024

Organized by

DEPARTMENT of ELECTRONICS AND COMMUNICATION ENGINEERING (NBA ACCREDITED)
BIT SINDRI, DHANBAD, JHARKHAND



ABOUT ELECTRONICS AND COMMUNICATION ENGINEERING DEPARTMENT

Vision

To be recognized as a center of academic excellence for globally competent, professionally, and socially responsible electronics & communication engineers and entrepreneurs.

Mission

To prepare students for a successful career by blending theoretical knowledge and practical skills with employability and entrepreneurial traits by offering UG, PG, and Doctoral programs in the Electronics & Communication Engineering domains.

To establish a state-of-the-art laboratory and research facilities for academic excellence and promotion of quality teaching as well as a learning process.

To inculcate team spirit and leadership qualities and produce socially acceptable engineers with ethical and human values.

To contribute to the country and society at large by enhancing the interaction between academia and industries for addressing the need of mankind.

ABOUT THE PROGRAMME

The objective of this workshop is to provide a hands-on learning experience using SDR for engineering students and faculty members who are interested in the design, implementation, and experimentation of a communication system. Software Defined Radio (SDR) is defined as a collection of hardware and software technologies where some or all of the radios operating functions are implemented through modifiable software operating on programmable processing platforms. The SDR platform acquires different radio personalities based on different waveforms.

Academia and researchers use Software Defined Radio as a teaching tool for wireless communication courses and as a platform to experiment with wireless communication systems. This course is aligned to the above themes and is designed around the Programmable platform for Experiments and Academic Research. With such an aptitude, the knowledge of tools of Communication system would enhance the practicing skills of the Faculty Members and students.

RESOURCE PERSON

Mr. S. Nayar, Technical Head, Academia Pune

WHO CAN ATTEND

Faculty members, Ph.D. scholars, PG and UG students from ECE, EE, EEE, CSE and IT departments, who wish to become familiar with the Software Defined Radio.

SCHEDULE & VENUE

Date: 15th June 2024

Schedule:- Registration: 09:00 AM – 09:30 AM Inauguration: 09:30 AM – 10:00 AM 1ST Session: !0:00 AM – 01:00 PM

2nd Session: 02:00 PM – 05:00 PM

Valedictory and Certificate Distribution: 05:00 – 05:30 PM

Venue:- Room No.: S-28, Second Floor, ECE Department, IT Building, BIT Sindri, Sindri, Jharkhand.

REGISTRATION FEE

There is no registration fee for participants

CERTIFICATION

A certificate will be provided to those participants who will attend the programme with 100% attendance.

ORGANIZING COMMITTME

Chief Patron

Prof. D.K. Singh, Vice Chancellor, JUT, Ranchi

Patron

Prof. Pankaj Rai, Director, BIT Sindri.

Convener

Prof. M.G. Tiary, HoD, Dept. of ECE, BIT Sindri.

Coordinators

Dr. Arvind Kumar, ECE Dept., BIT Sindri.

Dr. Prashant Kumar Singh, ECE Dept., BIT Sindri.

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