



# Internship on VLSI Design



‘Unveiling a Path to Hands-On Experience in Integrated Circuit Design’

Department of ECE, BIT Sindri, envisions a dream to foster curiosity, creativity and imagination in young minds and to create a pool of VLSI professionals to meet the huge industrial demand by organizing “Internship on VLSI Design”.

## Key Takeaways from VLSI Design Internship:

- Interns will gain hands-on experience and proficiency in utilizing various VLSI design tools for chip design and verification.
- Interns will acquire practical skills in designing and simulating circuits, enabling them to create efficient and reliable electronic systems.
- Interns will learn how to identify and rectify issues in circuits, as well as optimize them for better performance and functionality.

❖ **Schedule: 05 days a week starting from 17<sup>th</sup> June 2024 to 13<sup>th</sup> July 2024.**

❖ **Course fee: Rs. 1000/- payable to ‘student fund’ A/C No. 10635508860 IFSC: SBIN0011812.**

❑ **Hostel accommodation and fooding available on payment basis.**

### Convener:

**Dr. Praveen Kumar Sahu,**

Professor in charge of VLSI Design lab, Assistant Professor, Department of ECE, BIT Sindri



### Coordinator:

**Dr. Amar Prakash Sinha,** Associate Professor, Dept. of ECE, BIT Sindri



### Patron :

**Prof. Pankaj Rai,** Director, BIT Sindri



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**Advisory Committee:** Prof. V. N. Mishra, Professor, Dept. of Electronics Engineering, IIT (BHU), Prof. V. Kumar, Professor (Retd.) Dept of Electronics Engineering, IIT (ISM), Prof. Rajeev Kumar Ranjan, Associate Professor, IIT (ISM), Prof. Sunny Sharma, Assistant Professor, IIIT Allahabad, Prof. Shivam Verma, Assistant Professor, IIT (BHU), Prof. Kunal Singh, Assistant Professor, ECE, NIT Jamshedpur.

To register scan QR code/go to the link given below:

<https://forms.gle/t9A7WrizrRRrcheZ9>



**Technical Committee:** Prof. M. G. Tiary, Dr. Imteyaz Ahmad, Dr. Arvind Kumar, Dr. Prashant Kumar Singh, Prof. Gunjan Gandhi, Prof. J. S. Verma, Prof. S Kumari